



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of : Atty. Docket: 02-GR2-175
Lionel GRILLO : Group Art Unit: 2817
Serial No. 10/649,998 : Confirmation No. 6062
Filed: August 26, 2003 :
For: VARIABLE GAIN LOW-PASS FILTER :

SUBSTITUTE INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

The attached Form PTO-1449 provides a listing of information which may be relevant to the subject application. This IDS is not intended as a representation that better art is not available, nor that other art than that identified exists; nor that the information provided is prior art; nor that a search has been made.

This IDS is submitted under:

- ☒ XX 37 CFR 1.97(b) - No Fee.
☐ 37 CFR 1.97(c) - No Fee, with Certification.
☐ 37 CFR 1.97(c) - Fee.
☐ 37 CFR 1.97(d) - Fee, Certification & Petition.

The Commissioner is authorized to charge any required fees under 37 CFR 1.17(p) and (i) (1) to Deposit Account No. 50-1556.

Respectfully submitted,

Date: 6/13/05

By: 

Jon A. Gibbons
Registration No. 37,333

FLEIT, KAIN, GIBBONS, GUTMAN, BONGINI & BIANCO P.L.
551 NW 77th Street
Suite 111
Boca Raton, Florida 33487
Telephone: (561) 989-9811
Facsimile : (561) 989-9812

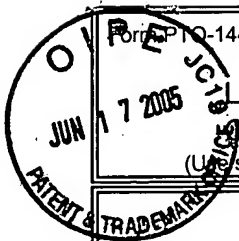
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service by first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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6/13/05
Date of Deposit



Form PTO-1449

U.S. Dept. of Commerce
Patent & Trademark Office

Atty. Docket: 02-GR2-175

Serial No. 10/649,998

List of Documents
Cited by Applicant
(Use several sheets if necessary)

Applicant: Lionel GRILLO

Filing Date: August 26, 2003

Group: 2817

U.S. PATENT DOCUMENTS

Ex'r's In'l	Document Number	Date	Name	Class	Sub- class	Filing Date, if applicable

FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class	Sub- class	Transl'n Yes/No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AA1	P.R. Gray et al. "Analysis and Design of Analog Integrated Circuits", March 2001, New York, Chapter 4, pp. 287-293 and Chapter 12, pp. 808-832, XP002241157.
AA2	Menolfi C. et al. "A Low-Noise CMOS Instrumentation Amplifier for Thermoelectric Infrared Detectors", IEEE Journal of Solid-State Circuits, IEEE, Inc. New York, Vol. 32, No. , July 1997, pp. 968-976, XP000729356.
AA3	Gopinathan V. et al. "Design Considerations and Implementation of a Programmable High-Frequency Continuous-Time Filter and Variable-Gain Amplifier in Submicrometer CMOS", IEEE Journal of Solid State circuits, IEEE Inc., New York, Vol. 34, No. 12, December 1999, pp. 1698-1707, XP000932500.
AA4	Daubert S. J. et al. "A Transistor-Only Current-Mode Sigma Delta Modulator", IEEE Journal of Solid State Circuits, IEEE Inc., New York, Vol. 27, No. 5, May 1992, pp. 821-830, XP000291092.
AA5	French Preliminary Search Report dated May 14, 2003 for French Application No. 0210751.

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.